

## ABSTRACT OF THE DISCLOSURE

A pulse modulated signal (ei) output from a pulse modulator (1) and a feedback signal (ef) containing distortion caused by a power switch (3) are integrated in a first  
5 integrator (21) and a second integrator (24), respectively, and are input to input terminals of a comparator (25 or 29), respectively, so that a correction signal (Vc) is generated.